

II PIEEE-77 BOARD

HOW TO OPERATE THE PIEEE-77 BOARD

Fig 21.1 shows the physical layout of your board. Let us start using it and learn about it as we go along.

(1) Touch a ground to discharge yourself. This is believed to save MOS chips in the board from being blown by high-voltage static discharge.

(2) Put a minigator clip lead to the TOUCH-POST.

(3) Plug in the power supply. One of the latch lights will go on; some other lights may or may not go on.

CONTROL PADS

(4) With the other end of your clip lead touch Reset, then Run pads. The Run and EXAMINE lights should go on, and probably some of the ADDRESS and DATA LIGHTS.

(5) Touch the SS (Single Step) pad. SS light should go on, RUN light should go off.

(6) Touch the HALT pad. The EXAMINE light remains on. Touch the LOAD pad. All 3 control lights should now be off.

EXAMINING CONTENTS OF RAM CHIPS

Each pair of 6111 RAM chips contains 256 8-bit registers. Each register contains a BYTE (8 bits) of data. For convenience we refer to 256 8-bit registers as a PAGE of memory. It takes 8 address bits to address the 256 registers within a page. These 8 address bits we shall refer to as the LINE address. Since the address bus consists of 16 lines, we will call the 8 more significant bits the PAGE address. The page address picks out a particular pair of RAM chips. The first pair of RAM chips on this board are wired to be addressed as page 00. Let us examine the contents of some of the registers on page 00 to see whether they have any data in them.

ADDRESS PADS

(7) Touch EXAMINE pad. Run the minigator clip lightly along the OFF pads of the ADDRESS lights. ADDRESS should now read zero, i.e., all ADDRESS lights should now be out. DATA lights now show the contents of register 0000.

BEFORE NEXT STEP UNPLUG POWER SUPPLY MOMENTARILY.

(8) Record below on DATA 1 line the light pattern in DATA lights using the hex code.

ADDRESS	0000	0001	0002	0003	0004	0005	0006
DATA 1
DATA 2

(9) Touch the ON pad of ADDRESS bit 0. That light should go ON. The hex pattern of address lights is now 0001. Record the DATA pattern. Repeat until the line above is filled.

(10) Unplug the power supply and plug it in again. Repeat from (6) above, this time entering in DATA 2 line. DATA 2 line should be more or less identical to the DATA 1 line.

In any case, this "data" is background noise due to natural processes during manufacturing of the RAM chips. NOTE: Usually this noise does not interfere with the operation of the board: just like an audio tape recorder, WRITING data into a register automatically erases what was there before; however, ADDING or SUBTRACTING data from a register -- on the assumption that there was zero there to begin with -- will result in an error, of course.

DATA PADS

(11) Before you go on to the next step, touch some of the DATA pads. Note that nothing happens to the lights -- data in RAM chips cannot be changed while the EXAMINE light is on.

(12) You touch the LOAD pad. The EXAMINE light should go out. The ADDRESS and DATA registers now display what you wish to load into the RAM chips. Note that now you can change both the address and data at will. (However, the RAM chip is not affected until the DEPOSIT pad is touched.)

(13) Set the ADDRESS to any address on page 0; i.e., starting with 00. EXAMINE data. Load some other data. Touch the DEPOSIT pad. EXAMINE again to verify that data has indeed been written into the RAM chips. Try it on several data.

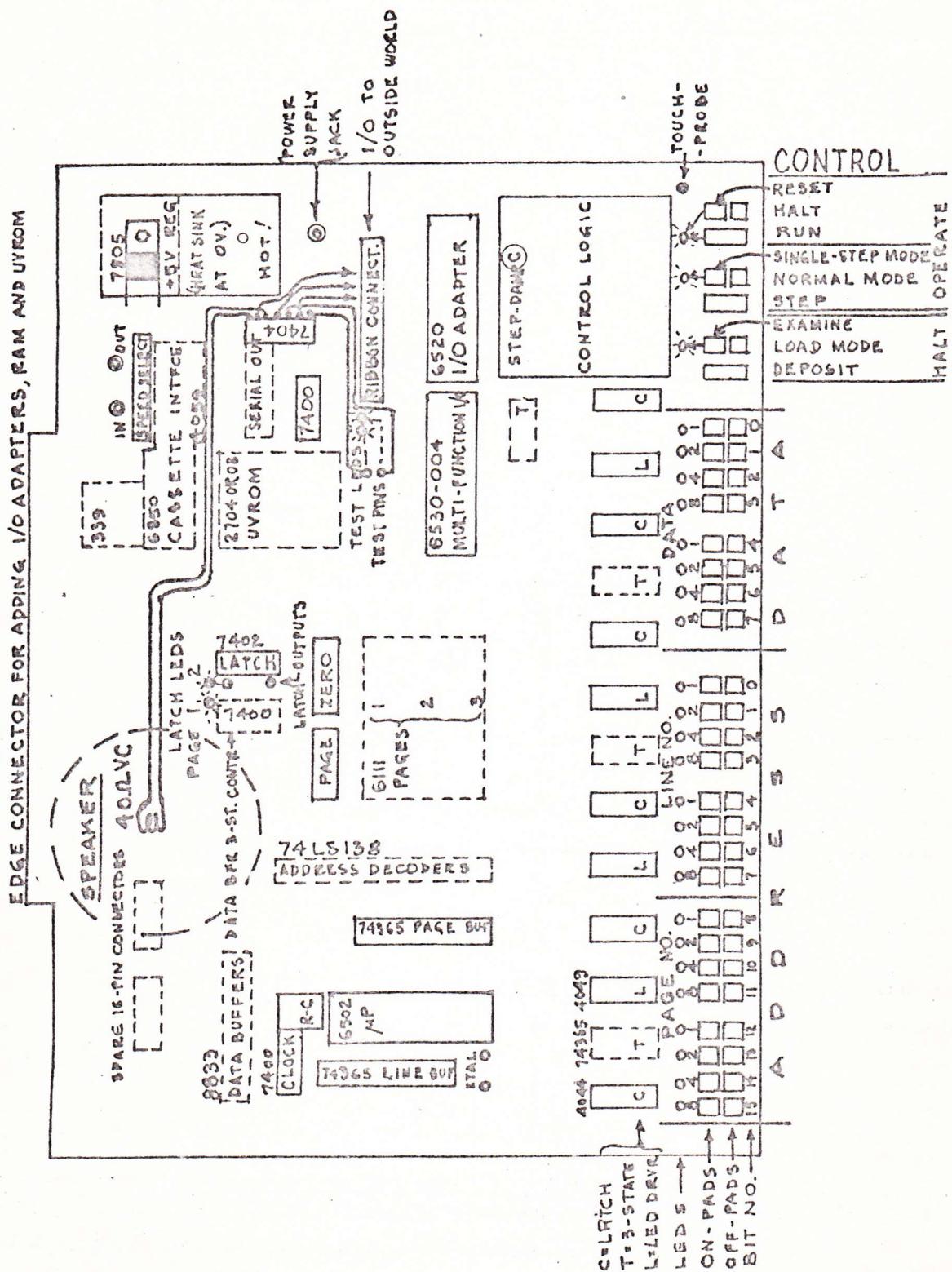


Fig. 21.1 Layout of PIEEE-77 board. Dotted chips and areas are not populated.

Line 1A it knows that it must now jump to Line 0018. Note the result: the uP will continuously jump from 001A to 0018, etc. We call it "jumping rope" because like a person jumping rope, the uP jumps and jumps, but stays in one place.

The purpose of jumping rope is to prevent the uP from reading the next line, 001B, finding some noise number there and interpreting it to mean some operation or other. This could be -- and usually is -- disastrous, since it can change the program just written and run itself into the ground. The 6502 does not have an op-code that will halt the uP. (It does, however, have a pin for halting it).

Line FFFC: When the Reset-and-Run pad is touched, the uP automatically looks at address FFFC to see with what line to start the program; the next address (FFFD) furnishes the page number. We have asked the uP to start at address 0010.

ENGINEERING LANGUAGE

Instead of commenting on the op-codes in long-hand, we can use algebra for most of them; for the others we can add some more-or-less obvious symbols which we will call engineering language. This, then, is what the program looks like with engineering language added:

Program 7*BIT*ADD

```

0010 D8 fd=0    clear Decimal flag
0011 18 fc=0    clear Carry flag
0012 A9 A=hh    load A with hh, where
0013 11 hh=11    hh=11
0014 69 A+fc+hh to Accumulator add fc and
               hh, where
0015 22 hh=22    hh=22
0016 85 (ZLL)=A store A in RAM register
               on page 0,
               line 00
0017 00 ll=00    line 00
0018 4C J:ppll  Jump to program
0019 18 ll=18    line 18
001A 00 pp=00    page 00
FFFC 10 J: 10    Jump to 10, i.e. start
               with line 10
               on page 00
FFFD 00 00

```

Program SEVEN-BIT-ADD

```

0010 DB -- -- fd #0    set binary mode   ). INITIALIZE
               )
0011 18 -- -- fc=0    clear Carry flag )
0012* A9 11 -- A=11    load first number into Accumulator
0014* 69 22 -- fc+22   add second number to Accumulator
0016* 85 00 -- (Z00)=A copy sum into register 0000
0018* 4C 18 00 jump rope
FFFC* -- 10 00 J:0010  start on line 0010

```

You will note that "f" stands for flag. A is Accumulator. hh is two hex digits. J: means jump to. pp means two digits for page number. ll means two hex digits for line number. () means the contents of. This is to avoid confusion between the look-alike number OF register (address) and number IN register (data). A complete list of engineering language symbols is in the frontispiece.

Line 0016: (Zll) means the contents of RAM register ll on page Zero. Notice that page Zero is inherent in the op-code 85; only the line number need be furnished.

SEGREGATING THE OP-CODES

Looking back at the last program listing, note that line 0012 contains an op-code followed by an uncoded hex number on the next line; line 0014 contains an op-code followed by an uncoded hex number on the next line; line 0018 has an op-code followed by uncoded numbers on next two lines. The program listing will become easier to read and to take in with a glance if the lines are rearranged so that every line either consists of an op-code or starts with one, thus:

NOTES

The * is a reminder that there is more than one address on this line. Note that now the first data column contains only op-codes; the second and third data columns contain constants.

Line 0018* illustrates a 6502 peculiarity; rather than the "natural" order of first the page, then the line. The 6502 requires first the line, then the page.

Note also the 2 comments on the right side: they give you a single-glance view of what procedure is followed (regardless of the particular microprocessor chip used). This is called a machine-independent program.

SUM=FIRST.NUMBER
SUM+SECOND.NUMBER

22.3 GENERATING A CONTINUOUS SQUARE WAVE ON PIA PIN

(A) Program SQ.W.ON.PIA.PIN

```

FFFFC* -- 10 00          J:START
0010* A9 01 -- 2 START A=01 ←
0012* 8D 01 6E 4          (6E01)=A
0015* A9 01 -- 2 TOP    A=01 ←
0017* 8D 00 6E 4          (6E00)=A
001A* A9 00 -- 2          A=00
001C* 8D 00 6E 4          (6E00)=A
001F* 4C 15 00 3          J:TOP →

```

$(6E01)=01$ OUTPUT=PIA.PIN
 $(6E00)=01$ loop
 $(6E00)=00$ ↑ PIA.PIN=ON
 PIAPIN=OFF
 pool

(B) INSTRUCTIONS

Connect scope to I/O pin A0 of 6530. This pin is available at ribbon connector pin 12.

(C) DISCUSSION

This is the same program as the previous one except that the output is now directed to pin A0 of the 6530 I/O adapter chip. Since there is only one LED -- not two as with the latch -- therefore it is difficult to observe whether there is indeed a square wave at the output. A scope should be connected to the A0 pin.

Line 0010* sets bit 0 for the next line.

Line 0012*: ^{6E}01 is the DIRECTION register of 6530 side A. When we make $(6E01)=01$, its pin A0 becomes an output; the 7 other pins become inputs.

Line 0015* sets bit 0 for the next line.

Line 0017*: 6E00 is the DATA register for 6530 side A. When we make $(6E00)=01$, pin A0 goes ON.

Line 001A* resets bit 0 for the next line.

Line 001C*: When we make $(6E00)=00$, pin A0 goes OFF.

(D) QUESTIONS

(1) If we wrote $(6E00)=FF$, what would happen to the program?

(2) Why are bits A1 to A7 inputs?

(3) How would the program be changed to read out in side B of 6530?

(4) How would the program be changed to read out in bit 7 of side A?

GENERATING AN AUDIO-FREQUENCY WAVE

(A) Program AUDIO.WAVE

FFFC* -- 10 00 J:START

```

0010 D8 -- START
A9 01 -- A=01
8D 01 6E 4 (6E01)=A
A2 FF -- 2 TOP X=FF
A9 01 -- 2 ON A=01
8D 00 6E 4 (6E00)=A
CA -- -- 2 X-1
DO F8 -- 3 count down
A2 FF -- 2 if #0,J:ON is count-down done?
A9 00 -- 2 OFF X=FF
8D 00 6E 4 (6E00)=A
CA -- -- 2 X-1
DO F8 -- 3 if #0,J:OFF is count-down done?
F0 EA -- 3 if =0,J:TOP if yes,start from top

```

USING A TOGGLE

(A) Program TOGGLE.AN.AUDIO.WAVE

FFFC* -- 10 00 J:START

```

0010 D8 -- -- START fd=0
A9 01 -- A=01
8D 01 6E 4 (6E01)=A
EE 00 6E 6 TOGL (6E00)+1
A2 FF -- 2 X=FF
CA -- -- 2 COUNT X-1
DO FB -- 2 if #0,J:COUNT
F0 FB -- 2 if =0,J:TOGL

```

GENERATING A SUB-AUDIO SQUARE WAVE

(A) Program TWO.NESTED.Loops

FFFC* -- 10 00 J:START

```

0010 D8 -- -- START fd=0
A9 01 -- A=01
8D 01 6E 4 (6E01)=A
EE 00 6E 6 TOGL (6E00)+1
A0 FF -- 2 Y=FF
A2 FF -- 2 T2 X=FF
CA -- -- 2 COUNT X-1
DO FD -- 3 if #0,J:COUNT
88 -- -- 2 Y-1
DO F8 -- 3 if #0,J:T2
F0 FI -- 3 if =0,J:TOGL

```

GENERATING A TIME DELAY

(A) Program FOUR.NESTED.Loops

FFFC* -- 10 00 J:START

```

0010 D8 -- -- START fd=0
0011 A9 01 -- A=01
8D 01 6E (6E01)=A
EE 00 6E TOGL (6E00)+1
A9 FF -- A=FF
8D 00 00 (0000)=A
A0 FF -- TIME3 Y=FF
A2 FF -- TIME2 X=FF
CA -- -- TIME1 X-1
DO FD -- if #0,J:TIME1
88 -- -- Y-1
DO F8 -- if #0,J:TIME2
CE 00 00 (0000)-1
DO FI -- if #0,J:TIME3
F0 E7 -- if =0,J:TOGL

```

STING

98.44
41

0000* ENTER address of first note
 0002 ENTER PERIOD.MULTIPLIER (say, 01)
 0003 reserve for DURATION
 0004 reserve for PERIOD
 0005 ENTER DURATION.MULTIPLIER (say, 05) *45*
 0006 ENTER REPEAT.ENABLE (Zero keeps a tune from repeating). *hhf0*
 0012 reserve for PERIOD.MULT.COUNTER
 0014 reserve for PERIOD.COUNTER
 0015 reserve for DURATION.MULT.COUNTER

ADRS OPC OPMD T LABEL ENGINEERING INSTRUCTIONS

FFF0 -- 1D 00 J:START
 001D A2 FF -- START X=FF
 001F 9A -- -- pS=X
 0020 20 3C 00 J:sub INITIALIZE
 23 A0 00 -- BEGIN Y=00 ←
 25 20 46 00 NEXT J:sub FETCH.DURATION
 28 C9 00 -- fl:A-0
 2A D0 04 -- if/0,J: PRIOD >
 2C A6 06 -- X=(206)
 2E D0 F3 -- if/0,J:BEGIN >
 30 20 4C 00 PRIOD J:sub FETCH.PERIOD ←
 33 20 5B 00 J:sub PLAY.NOTE
 36 20 87 00 J:sub INCREMENT.NOTE.POINTER
 39 4C 25 00 J:NEXT &PAGE pool

ALGORITHM

```

INITIALIZE
set NOTE.POINTER=0
loop [FETCH.DURATION]
if DURATION=0
  if REPEAT.ENABLE=ON
    exit this loop
  else
    PLAY.NOTE
    pool [INCREMENT.NOTE.POINTER&PAGE]
  
```

003C D8 -- -- fd=0 (clear decimal flag)
 3D 18 -- -- fc=0 (clear Carry)
 3E A9 01 -- A=01
 40 8D 01 6E (6E01)=A make PAO an output set BIT.A0=OUTPUT
 43 A9 00 -- A=00
 45 60 -- -- J:ret S . RETURN

0046 B1 00 --	A=((Z00*)+Y)	FETCH.DURATION	fetch DURATION
48 85 03 --	(Z03)=A		NOTE.POINTER+1
4A C8 -- --	Y+1		RETURN
4B 60 -- --	J:ret S		

004C B1 00 --	A=((Z00*)+Y)	FETCH.PERIOD	fetch PERIOD
4E 20 54 00	J:sub ADJUST.PERIOD		ADJUST.PERIOD
51 85 04 --	(Z04)=A		store PERIOD
53 60 -- --	J:ret S		RETURN

0054 C9 FF --	fl:A-FF	ADJUST.PERIOD	
56 F0 02 --	if=0,J:AEND >		if PERIOD/FF
58 69 01 --	A+1		↓ PERIOD+1
5A 60 -- --	AEND J:ret S ←		RETURN

005B A5 05 --	PLAY	PLAY.NOTE	
5D 85 15 --	A=(Z05)		
5F A5 03 --	TSET	A=(Z03) ←	loop [DURATION.MULTIPLIER]
61 8D 07 6E		(6E07)=A	set CLOCK=DURATION
64 EE 00 6E	TOGL	(6E00)+1 ←	loop set OUTPUT=TOGGLE
67 20 76 00 6		J:sub TIME.THE.PERIOD	TIME.THE.PERIOD
6A AD 07 6E 4		A=(6E07)	fetch CLOCK
6D C9 80 -- 2		fl:A-80	if CLOCK=DONE
6F D0 F3 -- 3+1		if/0,J:TOGL >	pool exit this loop
71 C6 15 -- 5		(Z15)-1	
73 D0 EA -- 3+1		if/0,J:PLAY >	
75 60 -- -- 6		J:ret S	RETURN

0076 A5 02 -- 3	A=(Z02)	TIME.THE.PERIOD	
78 85 12 -- 3	(Z12)=A		loop [PERIOD.MULT]
7A A5 04 -- 3 PRSET	A=(Z04) ←		loop [ADJUSTED.PERIOD]
7C 85 14 -- 3	(Z14)=A		pool
7E C6 14 -- 5 PERD	(Z14)-1 ←		
80 D0 FC -- 3+1	if/0,J:PERD >		
82 C6 12 -- 5	(Z12)-1		
84 D0 F4 -- 3+1	if/0,J:PRSET >		
86 60 -- -- 6	J:ret S		

0087 C8 -- --	Y+1	INCREMENT.NOTE.POINTER&PAGE	
88 D0 02 --	if/0,J:IEND >		if NOTE.POINTER=0
8A E6 01 --	(Z01)+1		then PAGE+1
8C 60 -- --	IEND J:ret S <		RETURN

Program MUSIC. An example of audio program is shown in Table 1.

THE STING dur.period	MORSE CODE	TICKING OF CLOCK
0090	20 E3	40 40 dah 00p4 c0 01
	20 D6	10 01 01 10 tick 1
	20 CA	20 40 dit c0 01
	40 80	10 01 01 10 tick 2
	20 CA	40 40 dah c0 01
	40 80	10 01 01 10 tick 3
	20 CA	20 40 dit c0 01
	20 30	30 01 01 10 tick 4
	20 80	40 40 dah c0 01
	20 72	10 01 01 10 tick 5
	20 6B	40 40 dah c0 01
	20 65	10 01 01 10 tick 6
	20 80	20 40 dit c0 01
	20 72	10 01 01 10 tick 7
	20 65	40 40 dah
	20 83	40 01 00 end flag
	20 72	
	20 80	
	20 72	

Table 1. Audio programs.

$$12\sqrt{2}=1.0594631$$

MUSICAL SCALE

NOTE HEX

C 48

B 4C

A# 51

A 55

G# 5B

G 60

F# 65

F 6B

E 72

D# 78

D 80

C# 88

C 90

B 98

A# A1

A AB

G# B5

G BF

F# CA

F D6

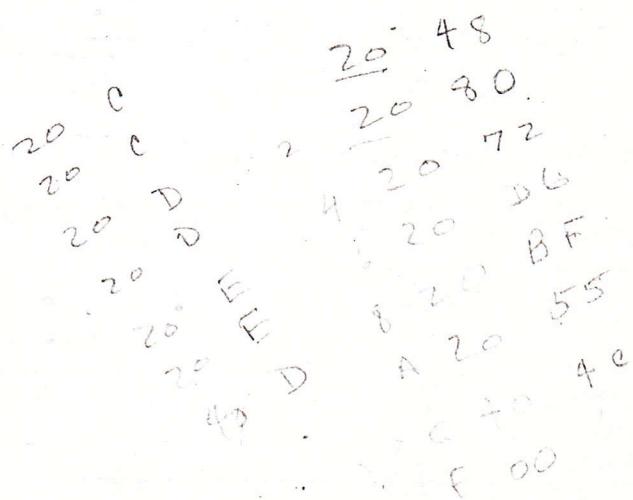
E E3

D# F1

D FF

HALF-TONE=

1.0594631x



PROGRAMMING: ARITHMETIC

ADDING TWO SEVEN-BIT NUMBERS

(A) Program SEVEN.BIT.ADD.1

```

FFFC* -- 10 00      J:0010 (START)

0010  D8 -- -- START fd=0 _____ start
0011  18 -- -- fc=0 _____
0012* A9 11 -- A=11 _____ SUM=FIRST.NUMBER
0014* 69 22 -- A+fc+22 _____ SUM+SECOND.NUMBER
0016* 85 00 -- (Z00)=A _____ SUM.DISPLAY=SUM
0018* 4C 18 00     J rope _____ end

```

(B) INSTRUCTIONS

Load the program. RESTART. HALT. Examine 0000 for sum. Enter two other numbers into 0013 and 0015. RESTART and HALT again. Examine 0000 for sum.

(C) QUESTIONS

- (1) What is the difference between this program and the one that was used in section 21 to learn the board?
- (2) In what registers are the two numbers to be added?
- (3) What would happen if there was no fd=0 instructions?
- (4) What would happen if there was no fc=0 instruction?
- (5) On line 0012, what is the meaning of operand 11?

- (6) In line 0014, what is the meaning of operand 22?
- (7) In line 0014, why are we adding fc to the sum?
- (8) In line 0016, why do we place the sum into register 0000?
- (9) In line 0018, when does the uP actually perform the jump?
- (10) Why is this a 7-bit program, not 8-bit?
- (11) In the algorithm column, why make the sum equal to the first number?
- (12) What is the meaning of the asterisk?
- (13) What other way is there of clearing fc and fd?
- (14) Would you clear the whole flag register if there was a subtraction operation ahead?
- (15)

ADDING TWO UNSIGNED SEVEN-BIT NUMBERS FROM PARAMETER REGISTERS

(A) Program SEVL1-BIT.ADD.2

```

0001 ENTER FIRST number
0002 ENTER SECOND number
0000 reserve for SUM.DISPLAY

```

```

FFFC* -- 10 00      J:START _____ start
0010  D8 -- -- START fd=0      set binary mode
0011  18 -- -- fc=0      clear Carry flag
0012* AD 01 00      A=(0001) _____ SUM=FIRST
0015* 6D 02 00      A+fc+(0002) _____ SUM+SECOND
0018* 8D 00 00      (0000)=A _____ SUM.DISPLAY=SUM
001B* 4C 1B 00     J rope _____ end

```

(B) INSTRUCTIONS

Load the program. Enter two numbers into registers 0001 and 0002. RESTART. HALT. Examine register 0000 for sum.

(C) QUESTIONS

- (1) What is the difference between this and the previous program, 23.10?
- (2) 0012: what is the difference in this instruction?
- (3) 0015: what is the difference in this instruction?
- (4) 0015: why do we add in the fc-- why not just leave it out?

ADDING TWO UNSIGNED NUMBERS I

(A) Program ADD

```

FFFC* -- 10 00      J:START

0001 ENTER number to be added, FIRST.NUMBER
0002 ENTER number to be added, SECOND.NUMBER
0003 reserve for Carry display
0004 reserve for SUM display

0010 D8 -- -- 2 START fd=0 _____ start
0011 18 -- -- 2 fc=0
0012* AD 01 00 4 A=(0001)
0015* 6D 02 00 4 A+fc+(0002) _____ SUM=FIRST.NUMBER+SECOND.NUMBER
0018* 8D 04 00 4 (0004)=A _____ SUM.DISPLAY=SUM
001B* A9 00 -- 2 A=0
001D* 69 00 -- 2 A+fc+00
001F* 8D 03 00 4 (0003)=A _____ CARRY.DISPLAY=CARRY
0022* 4C 22 00 3 J rope _____ end

```

(B) INSTRUCTIONS

Enter program. Enter two numbers to be added. RESTART. HALT. Examine register 0003 for Carry, 0004 for sum.

(C) QUESTIONS

- (1) If there is no Carry, how will that show?
- (2) If there is a Carry, how will that show?
- (3) 0012: is there another instruction that could have been used to accomplish the same thing? What are the advantages and disadvantages?

ADDING TWO DECIMAL NUMBERS

(A) Program ADD.TWO.DECIMAL.NUMBERS

```

FFFFC* -- 00 00      J:START
0002 F8 -- -- START fd=1
0003 18 -- -- fc=0
0004* A9 d1 -- A=d1
0006* 69 d2 -- A+fc+d2 _____ SUM=FIRST.DEC.NO+SECOND.DEC.NO
0008* 85 01 -- (Z01)=A _____ SUM.DISPLAY=SUM
000A* B0 03 -- if fc=1,J:CARRY>
000C* 4C 0C 00 J rope _____ end
000F* E6 00 -- CARRY (Z00)+1 _____ CARRY.DISPLAY=CARRY
0011* 4C 11 00 J rope _____ end

```

(B) INSTRUCTIONS

Load program. Enter the two decimal numbers into registers 0005 and 0007. RESTART. HALT. Examine 0000 for Carry, 0001 for sum.

Note that in this program no room was left at the top of the page for parameters, and the program starts with line 0002.

(C) DISCUSSION

This is a demonstration of how the decimal mode operates in the 6502. One problem with the decimal mode is that there is no flag to alert you in case you have entered a hex number instead of a decimal by mistake. In present-day uP practice there does not appear to be much use for the decimal mode.

(D) QUESTIONS

- (1) What is the % register utilization of the decimal mode?
- (2) What tells the uP to do this problem with decimal numbers?

ADDING TWO UNSIGNED NUMBERS II

(A) Program ADD.UNSIGNED.NUMBERS

0001 ENTER FIRST.NUMBER
 0002 ENTER SECOND.NUMBER
 0003 reserve for CARRY.DISPLAY
 0004 reserve for SUM.DISPLAY

FFFFC* -- 10 00 J:START

```

0010 D8 -- -- START fd=0
0011 18 -- -- fc=0
0012* A5 01 -- A=(Z01) SUM=FIRST.NUMBER
0014* 65 02 -- A+fc+(Z02) SUM+SECOND.NUMBER
0016* 85 04 -- (Z04)=A SUM.DISPLAY=SUM
0018* 90 07 -- if fc=0, J:NOCAR if CARRY=YES
001A* A9 01 -- A=01
001C* 85 03 -- (Z03)=A CARRY.DISPLAY=CARRY
001E* 4C 1E 00 J rope end
0021* A9 00 -- NOCAR A=00
0023* 85 03 -- (Z03)=A CARRY.DISPLAY=NO.CARRY
0025* 4C 25 00 J rope end
  
```

ADDING TWO SIGNED OR UNSIGNED NUMBERS

(A) Program ADD.SIGNED.OR.NOT

0001 ENTER FIRST.NUMBER (such as 78)
 0002 ENTER SECOND.NUMBER (such as FA)
 0003 use for UNSIGNED.CARRY.DISPLAY
 0004 use for SIGNED.OVERFLOW.DISPLAY
 0005 use for SUM.DISPLAY

FFFC* -- 10 00 J:START

```

0010 D8 -- -- 2 START fd=0
0011 18 -- -- 2 fc=0
0012* AD 01 00 4 A=(0001) SUM=FIRST.NUMBER
0015* 6D 02 00 4 A+fc+(0002) SUM+SECOND.NUMBER
0018* 8D 05 00 4 (0005)=A SUM.DISPLAY=SUM
001B* A9 00 -- 2 A=00
001D 2A -- -- 2 Afc
001E* 8D 03 00 4 (0003)=A UNSIGNED.CARRY.DISPLAY=CARRY
0021* A9 00 -- A=00
0023* 8D 04 00 (0004)=A SIGNED.OVERFLOW.DISPLAY=NONE
0026* 50 FE -- if fv=0, J rope if OVERFLOW=NONE, end
0028* A9 FE -- A=FE
002A* 8D 04 00 (0004)=A SIGNED.OVERFLOW.DISPLAY=OVERFLOW
002D* D0 FE -- if /0, J rope end
  
```

ADDING TWO 16-BIT NUMBERS (UNSIGNED)

(A) Program ADD.16-BIT

0000 ENTER FIRST.HIGH
 0001 ENTER FIRST.LOW
 0002 ENTER SECOND.HIGH
 0003 ENTER SECOND.LOW
 0004 reserve for CARRY.DISPLAY
 0005 reserve for SUM.HIGH.DISPLAY
 0006 reserve for SUM.LOW.DISPLAY

FFFC* -- 10 00 J:START start

```

0010 D8 -- -- 2 START fd=0
0011 18 -- -- 2 fc=0
0012* AD 01 00 4 A=(0001) SUM.LOW=FIRST.LOW
0015* 6D 03 00 4 A+(0003) SUM.LOW+SECOND.LOW
0018* 8D 06 00 4 (0006)=A SUM.LOW.DISPLAY=SUM.LOW
001B* AD 00 00 4 A=(0000) SUM.HIGH=FIRST.HIGH
001F* 6D 02 00 4 A+(0002) SUM.HIGH+SECOND.HIGH
0022* 8D 05 00 4 (0005)=A SUM.HIGH.DISPLAY=SUM.HIGH
0025* A9 00 -- A=00
0027* 69 00 -- A+fc+00
0029* 8D 04 00 (0004)=A CARRY.DISPLAY=CARRY
002B* 4C 2B 00 J rope end
  
```

ADDING TWO 16-BIT NUMBERS, SIGNED OR NOT

(A) Program ADD.16-BIT.SIGNED/UNSIGNED

0000 ENTER FIRST.HIGH
 0001 ENTER FIRST.LOW
 0002 ENTER SECOND.HIGH
 0003 ENTER SECOND.LOW
 0004 reserve for UNSIGNED.CARRY.DISPLAY
 0005 reserve for SIGNED.OVERFLOW.DISPLAY
 0006 reserve for SUM.HIGH.DISPLAY
 0007 reserve for SUM.LOW.DISPLAY

FFF0* -- 10 00 J:START

0010 D8-- -- START	fd=0	start
0011 18 -- --	fc=0	
0012* AD 01 00	A=(0001)	SUM.LOW=FIRST.LOW
0015* 6D 03 00	A+(0003)	SUM.LOW+SECOND.LOW
0018* 8D 07 00	(0007)=A	SUM.LOW.DISPLAY=SUM.LOW
001B* AD 00 00	A=(0000)	SUM.HIGH=FIRST.HIGH
001E* 6D 02 00	A+(0002)	SUM.HIGH+SECOND.HIGH
0021* 8D 06 00	(0006)=A	SUM.HIGH.DISPLAY=SUM.HIGH
0024* A9 00 --	A=00	
0026 2A -- --	Afc \hookrightarrow	
0027* 8D 04 00	(0004)=A	UNSIGNED.CARRY.DISPLAY=CARRY
002A* A9 00 --	A=00	
002C* 8D 05 00	(0005)=A	SIGNED.OVERFLOW.DISPLAY=NONE
002F* 50 FE --	if fv=0,J rope	if OVERFLOW=NONE, end
0031* A9 FE --	A=FE	
0033* 8D 05 00	(0005)=A	SIGNED.OVERFLOW.DISPLAY=OVERFLOW
0036* D0 FE --	if /0,J rope	end

ADDING TWO 24-BIT NUMBERS, SIGNED OR NOT

(A) Program ADD.24-BIT.SIGNED/UNSIGNED

0000 ENTER FIRST.HIGH
 0001 ENTER FIRST.MIDL
 0002 ENTER FIRST.LOW
 0003 ENTER SECOND.HIGH
 0004 ENTER SECOND.MIDL
 0005 ENTER SECOND.LOW
 0006 reserve for UNSIGNED.CARRY.DISPLAY
 0007 reserve for SIGNED.OVERFLOW.DISPLAY
 0008 reserve for SUM.HIGH.DISPLAY
 0009 reserve for SUM.MIDL.DISPLAY
 000A reserve for SUM.LOW.DISPLAY

FFF8* -- 10 00 J:START start

0010 D8 -- -- START	fd=0	
0011 18 -- --	fc=0	
0012* AD 02 00	A=(0002)	SUM.LOW=FIRST.LOW
0015* 6D 05 00	A+(0005)	SUM.LOW+SECOND.LOW
0018* 8D 0A 00	(000A)=A	SUM.LOW.DISPLAY=SUM.LOW
001B* AD 01 00	A=(0001)	SUM.MIDL=FIRST.MIDL
001E* 6D 04 00	A+(0004)	SUM.MIDL+SECOND.MIDL
0021* 8D 09 00	(0009)=A	SUM.MIDL.DISPLAY=SUM.MIDL
0024* AD 00 00	A=(0000)	SUM.HIGH=FIRST.HIGH
0027* 6D 03 00	A+(0003)	SUM.HIGH+SECOND.HIGH
002A* 8D 08 00	(0008)=A	SUM.HIGH.DISPLAY=SUM.HIGH
002D* A9 00 --	A=00	
002F 2A -- --	Afc \hookrightarrow	
0030* 8D 06 00	(0006)=A	UNSIGNED.CARRY.DISPLAY=CARRY
0033* A9 00 --	A=00	
0035* 8D 07 00	(0007)=A	SIGNED.OVERFLOW.DISPLAY=NONE
0038* 50 FE --	if fv=0,J rope	if OVERFLOW=NONE, end
003A* A9 FE --	A=FE	
003C* 8D 07 00	(0007)=A	SIGNED.OVERFLOW.DISPLAY=OVERFLOW
003F* D0 FE --	if /0,J rope	end

(A) Program ADD.4-DIGIT.DECIMAL.NUMBERS

```

0000 ENTER FIRST.HIGH
0001 ENTER FIRST.LOW
0002 ENTER SECOND.HIGH
0003 ENTER SECOND.LOW
0005 reserve for CARRY.DISPLAY
0006 reserve for SUM.HIGH.DISPLAY
0007 reserve for SUM.LOW.DISPLAY

```

(Handwritten notes above the program listing)

```

FFFFC* -- 10 00      J:START      start

0010 F8 -- -- START fd=1 set decimal mode
0011 18 -- -- fc=0
0012* AD 01 00 A=(0001) SUM.LOW=FIRST.LOW
0015* 6D 03 00 A+(0003) SUM.LOW+SECOND.LOW
0018* 8D 07 00 (0007)=A SUM.LOW.DISPLAY=SUM.LOW

001B* AD 00 00 A=(0000) SUM.HIGH=FIRST.HIGH
001E* 6D 02 00 A+(0002) SUM.HIGH+SECOND.HIGH
0021* 8D 06 00 (0006)=A SUM.HIGH.DISPLAY=SUM.HIGH

0024* A9 00 -- A=00
0026* 69 00 -- A+fc+00
0028* 8D 06 00 (0006)=A CARRY.DISPLAY=CARRY
002B* A9 00 -- A=00
002D* F0 FE -- if =0,J rope end

```

#2 MULTIPLYING TWO HEX DIGITS BY SUCCESSIVE ADDITION

(A) Program MULT.2.DIGITS.BY.ADDITION

```

0001 ENTER FIRST digit thus: 0X
0002 ENTER SECOND digit, thus: 0Y
0003 reserve for PRODUCT.DISPLAY

```

```

FFFFC* -- 10 00      J:START      start

0010 18 -- -- START fc=0
0011 06 -- -- fd=0
0012* A6 02 -- X=(0002)
0014* A9 00 -- A=00 PRODUCT=0
0016* E9 00 -- fl:X-00
0018* F0 07 -- if =0,J:STORE
001A* 05 01 -- CUMUL A+fc+(Z01) loop[SECOND]
001C* CA -- -- X-1 PRODUCT+FIRST
001D* D0 F0 -- if /0,J:CUMUL pool
001F* EA EA
0021* 65 00 -- STORE (0003)=A PRODUCT.DISPLAY=PRODUCT
0023* 4C 23 00 J rope end

```

#3 DETERMINING WHICH OF TWO UNSIGNED NUMBERS IS LARGER

(A) Program COMPARE

```

0000 reserve for DISPLAY.RESULT (if FIRST is larger, 01; if second, 02; if equal, 03)
0001 ENTER FIRST number
0002 ENTER SECOND number

```

```

FFFFC* -- 10 00      J:START

0010 D8 -- -- START fd=0
0011 38 -- -- fc=1
0012* A9 00 -- A=00
0014* 85 00 -- (Z00)=A
0016* A5 01 -- A=(Z01)
0018* C5 02 -- fl:A-(Z02) try FIRST-SECOND
001A* F0 10 -- if =0,J:EQUAL if /0
001C* B0 07 -- if fc=1,J:FIRST if BORROW=YES
001E* A9 02 -- A=02 DISPLAY=SECOND.IS.LARGER; end
0020* 85 00 -- (Z00)=A also DISPLAY=FIRST.IS.LARGER; end
0022* 4C 22 00 J rope else DISPLAY=EQUAL; end
0025* A9 01 -- FIRST A=01
0027* 85 00 -- (Z00)=A
0029* 4C 29 00 J rope
002C* A9 03 -- EQUAL A=03
002E* 85 00 -- (Z00)=A
0030* 4C 30 00 J rope

```

#1 GENERATING A RANDOM NUMBER

(A) Program ULTRA.SIMPLE.RANDOM.NOISE.GENERATOR

0000 store random number

FFFC* -- 10 00 J:START

0010* C6 00 -- START (ZOO)-1
0012* 4C 10 00 J:START

loop RANDOM.NUMBER-1
pool

(B) PROCEDURE

Enter program, set switch register to 0020. RESET, RUN, EXAMINE. RUN and EXAMINE again.

(C) DISCUSSION

This program decrements a register until you halt it to examine. It will then display a random number between 00 and FF. The number is random because the uP loops

many thousands of times before you can possibly stop it.

Advantage is taken of the fact that the switch register at the PIEEE-77 board is latched to the register it examined last. By repeatedly touching RUN and EXAMINE, you can generate a sequence of random numbers.

#2 GENERATING A DECIMAL RANDOM NUMBER WITHIN STATED LIMITS

(A) Program DECIMAL.RAND.NUM.GEN

0000 reserve for DISPLAY

01 ENTER upper limit UPLIM, such as 77
02 ENTER lower limit LWLIM, such as 43

ENTER: UPPER.LIMIT as decimal number
ENTER: LOWER.LIMIT as decimal number

```
FFFC* -- 10 00      J:START
0010  F8 -- --      FD=1
0011  38 -- --      FC=1
12  A5 01 -- RESET A=(Z01)    loop COUNTER=UPPER.LIMIT
E9 01 -- DECRM A-FC/-1      loop COUNTER-1
85 00 --             (Z00)=A
C5 02 --             fl:A-(Z02)
D0  F8 --             if/0,J:DECRM
4C 12 00             J:RESET   pool if COUNTER=LOWER.LIMIT, exit this loop
                                pool
```

(B) PROCEDURE: Enter UPPER.LIMIT, LOWER.LIMIT; set address to 0000. RESET, RUN, HALT. EXAMINE: Data readout now displays a random decimal number between UPPER.LIMIT AND LOWER.LIMIT To generate another random number, RESTART and HALT.

3 This generator is not truly random. It shows a bias in favor of LOWER.LIMIT. Why? What% bias?

4 What will make the numbers more truly random?

(C) DISCUSSION: This program counts down a register over and over again. The number remaining in the register when the computer is stopped is random because a random number of clock cycles has passed. The switch register remembers the register it looked at last, and the program takes advantage of this to display the result without the need to readdress it each time the program is run.

(D) QUESTIONS

1 What happens if a non-decimal number is entered for UPPER.LIMIT? for LOWER.LIMIT?

2 What happens if LOWER.LIMIT is larger than UPPER.LIMIT?